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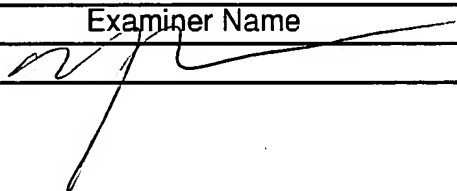
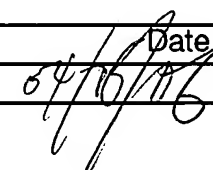
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Title of Invention	FIELD PROGRAMMABLE GATE ARRAY AND MICROCONTROLLER SYSTEM ON A CHIP																				
<p>Application Number: 10/821533 *10/821533*</p> <p>Confirmation Number: 1284</p> <p>First Named Applicant: Arunangshu Kundu et al</p> <p>Attorney Docket Number:</p> <p>Search string: (5854752).pn.</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td><i>n</i></td><td>1</td><td>5854752</td><td>1998-12-29</td><td>Agarwal</td><td>A</td><td>364</td><td>489</td></tr></tbody></table> <p>Signature</p> <table border="1"><tr><td>Examiner Name</td><td>Date</td></tr><tr><td><i>[Signature]</i></td><td><i>[Signature]</i></td></tr></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	<i>n</i>	1	5854752	1998-12-29	Agarwal	A	364	489	Examiner Name	Date	<i>[Signature]</i>	<i>[Signature]</i>
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